

CLOCK SWITCHING CIRCUITABSTRACT OF THE DISCLOSURE

5 A clock switching circuit for switching a first
clock signal being output to a freely selected second
clock signal among a plurality of clock signals having
different frequencies and phases while preventing
generation of a hazard, comprising a plurality of unit
10 circuits for respectively receiving as an input the clock
signals, selection signals of the clock signals and
enabling signals and controlling supplying and stopping
of the clock signals in accordance with the selection
signals and the enabling signals; and a feedback circuit
15 for monitoring output conditions of the plurality of unit
circuits and, when outputting of all clock signals of the
plurality of unit circuits was stopped as a result of
stopping the first clock signal, giving a plurality of
the unit circuits the enabling signals for approving
20 starting of a supply of the second clock signal.